



Docket No.: F9600.0022/P022  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Mitsuaki Hirono et al.

Confirmation No. 3986

Application No.: 09/195,125

Allowed: May 13, 2004

Filed: November 18, 1998

Art Unit: 2126

For: PROGRAM CONTROL APPARATUS AND  
METHOD AND APPARATUS FOR  
MEMORY ALLOCATION ENSURING  
EXECUTION OF A PROCESS  
EXCLUSIVELY AND ENSURING REAL  
TIME OPERATION, WITHOUT  
LOCKING COMPUTER SYSTEM

Examiner: L. B. Zhen

**SECOND REQUEST FOR ACKNOWLEDGEMENT OF  
INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Applicants' representative filed a (first) Request for Acknowledgement of Information Disclosure Statement on May 27, 2004. The Request was filed because an IDS originally filed on January 6, 1999 had not been acknowledged. The May 27, 2004 Request included a copy of the January 6, 1999 IDS and a copy of a stamped postcard indicating that the January 6, 1999 IDS had been received by the USPTO. Applicant's representative has not received a response to the Request of May 27, 2004.

Supervisory Patent Examiner Meng An indicated that the file wrapper of the instant application has been unavailable to the examiner since shortly after the mailing

of the Notice of Allowance. Ms. An advised paying the issue fee, stating that the January 6, 1999 IDS will be considered when the file becomes available. Accordingly, applicant's representative is filing this (second) Request for Acknowledgement of Information Disclosure Statement. A copy of the (first) Request is enclosed, including a stamped postcard indicating that the first Request had been received by the USPTO. A copy of the references cited in the IDS is also enclosed.

Dated: August 12, 2004

Respectfully Submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Christopher S. Chow

Registration No.: 46,493

DICKSTEIN SHAPIRO MORIN &

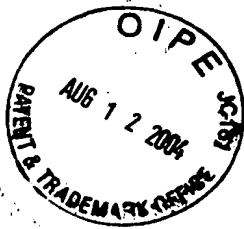
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicants



Atty Docket No.: F9600.0022/P022

Inventor: Mitsuaki Hirono et al.

Application No.: 09/195,125-Conf. #3986 Filing Date: November 18, 1998  
Title: PROGRAM CONTROL APPARATUS AND METHOD AND APPARATUS FOR  
MEMORY ALLOCATION ENSURING EXECUTION OF A PROCESS  
EXCLUSIVELY AND ENSURING REAL TIME OPERATION, WITHOUT  
LOCKING COMPUTER SYSTEM

Documents Filed:  
Request for Acknowledgement of Information Disclosure Statement (2 pages) with copy of 1/6/1999  
Information Disclosure Statement filing and date-stamped postcard (5 pages)

Via: PTO DAILY RUN

Sender's Initials: TJD/CSC/cdl

Date: May 27, 2004



m.s.



Docket No.: F9600.0022/P022  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Mitsuaki Hirono et al.

Confirmation No. 3986

Application No.: 09/195,125

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Art Unit: 2126

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MEMORY ALLOCATION ENSURING  
EXECUTION OF A PROCESS  
EXCLUSIVELY AND ENSURING REAL  
TIME OPERATION, WITHOUT  
LOCKING COMPUTER SYSTEM

Examiner: L. B. Zhen

**REQUEST FOR ACKNOWLEDGEMENT OF  
INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In the Notice of Allowability dated May 13, 2004, there is no indication that the Information Disclosure Statement filed on January 6, 1999 has been considered. Applicants respectfully request that the receipt and consideration of this Information Disclosure Statement (IDS) be formally acknowledged at the earliest possible convenience. For convenience, attached hereto is a copy of the IDS, Form PTO SB/08 and the corresponding postcard receipt with date-stamp showing the date of filing of the above listed Information Disclosure Statement.

Application No.: 09/195,125

Docket No.: F9600.0022/P022

Dated: May 27, 2004

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Christopher S. Chow

Registration No.: 46,493

DICKSTEIN SHAPIRO MORIN &  
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Washington, DC 20037-1526

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Attorneys for Applicants



Atty Docket No.: F9600.022/P022

Inventor: Mitsuaki Hirono et al.

Serial No.: 09/195,125

Filing Date: November 18, 1998

Title: PROGRAM CONTROL APPARATUS AND METHOD AND APPARATUS FOR  
MEMORY ALLOCATION ENSURING EXECUTION OF A PROCESS  
EXCLUSIVELY AND ENSURING REAL TIME OPERATION, WITHOUT  
LOCKING COMPUTER SYSTEM

Documents Filed: Claim for Priority and Submission of Documents; Information  
Disclosure Statement; Form PTO 1449; and cited references



Sender's Initials: TJD/las

Date: January 6, 1999



PATENT  
Docket No.: F9600.022/P022

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Mitsuaki Hirono et al.

Serial No.: 09/195,125

Group Art Unit: 2751

Filed: November 18, 1998

Examiner: Not Yet Assigned

For: PROGRAM CONTROL  
APPARATUS AND METHOD  
AND APPARATUS FOR  
MEMORY ALLOCATION  
ENSURING EXECUTION OF  
A PROCESS EXCLUSIVELY  
AND ENSURING REAL TIME  
OPERATION, WITHOUT  
LOCKING COMPUTER  
SYSTEM

Assistant Commissioner for Patents  
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT (IDS)

Dear Sir:

Pursuant to 37 C.F.R. 1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. A copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- x 1. This Information Disclosure Statement is being filed within three months of the U.S. filing date OR within three months of the date of entry of the national stage as set forth in 37 C.F.R. 1.491, OR before the mailing date of a first Office Action on the merits, whichever event occurs last. No certification or fee is required. 37 C.F.R. 1.97(b).

2. This Information Disclosure Statement is being filed more than three months after the U.S. filing date, OR more than three months after the date of entry of the national stage, AND after the mailing date of the first Office Action on the merits, whichever occurs first, but before the mailing date of a Final Rejection or Notice of Allowance. 37 C.F.R. 1.97(c).

a. I hereby certify that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. 1.97(e)(1).

b. I hereby certify that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. 1.97(e)(2).

c. Attached is our check in the amount of \_\_\_\_\_ in payment of the fee under 37 C.F.R. 1.17(p). Please charge any fee deficiencies, or credit any overpayment, to Deposit Account No. 04-1073 as needed to ensure consideration of the Information Disclosure Statement. Two duplicate copies of this paper are attached.

3. This Information Disclosure Statement is being filed more than three months after the U.S. filing date, OR more than three months after the date of entry of the national stage, AND after the mailing date of a Final Rejection or Notice of Allowance, but before payment of the Issue Fee. Applicant(s) hereby petition(s) that the Information Disclosure Statement be considered. Attached is our check in the amount of \_\_\_\_\_ in payment of the petition fee under 37 C.F.R. 1.17(i)(1). Please credit or debit Deposit Account No. 04-1073 as needed to ensure consideration of the Information Disclosure Statement. Two duplicate copies of this paper are attached.

a. I hereby certify that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. 1.97(e)(1).

b. I hereby certify that no item of information in this



Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. 1.97(e)(2).

- \_\_\_\_\_ 4. Translations of the non-English language references are enclosed.  
        X   Full (for the non-English articles)  
      \_\_\_\_\_ Partial  
        X   Summary/abstract (for JP '333)
- \_\_\_\_\_ 5. A copy of a foreign application communication from a related foreign application discussing the relevancy of the references is attached.

Dated: January 6, 1999

Respectfully submitted,

By \_\_\_\_\_

Thomas J. D'Amico

Registration No.: 28,371

DICKSTEIN SHAPIRO MORIN

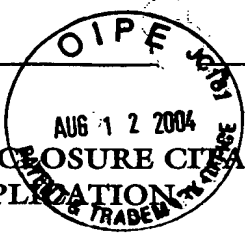
& OSHINSKY LLP

2101 L Street, NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicants



**INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION**

Docket Number		Application Number	
F9600.022/P022		09/195,125	
Applicant(s)			
Mitsuaki Hirono et al.			
Filing Date		Group Art Unit	
November 18, 1998		2751	

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	REF	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

**FOREIGN PATENT DOCUMENTS**

	REF	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	Translations	
							YES	NO
	A	3-231333	10/15/91	Japan			Abstr.	

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

	B	Taiichi Yuasa, "Algorithms for Realtime Garbage Collection," Jyouhoushori, November 1994 w/translation.					
	C	Yuji Chiba et al., "An Implementation of a Control Stack on Standard ML of New Jersey," Proceedings of 12 <sup>th</sup> Conference of Japan Society for Software Science and Technology w/translation.					
	D	Lu Xu et al., "Distributed Garbage Collection for Parallel Inference Machine: PIE64," 38 <sup>th</sup> (1989 First Term) National Conference of Information Processing Society of Japan, pgs. 1563-1564.					

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.